

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"4770841".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:09
L2	1	"4770841".pn. and data	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:30
L3	0	"4770841".pn. and status	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:34
L4	0	"4770841".pn. and status	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:34
L5	0	"4770841".pn. and (basis or status)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:40
L6	11295	(dynamic same control same system) and (control same status)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:40
L7	198	(dynamic same control same system) and (control same status) and (manipulated same variable)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:41

L8	88	(dynamic same control same system) and (control same status) and (manipulated adj variable)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:41
L9	21	(dynamic same control same system) and (control same status) and (manipulated adj variable) and circuits	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/04/17 12:42
L10	35	(dynamic same control same system) and (control same status) and (manipulated adj variable) and ( control same circuits)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 12:42
L11	194	700/83.ccls. and (state same variable)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 12:55
L12	136	700/83.ccls. and (state same variable) and status	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 12:55
L13	26	700/83.ccls. and (state same variable) and status and (circuits same design)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 12:56
L14	24	700/83.ccls. and (state same variable) and status and (circuits same design) and machine	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 12:57

L15	23	700/83.ccls. and (state same variable) and status and (circuits same design) and (machine same control)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 12:57
L16	0	700/83.ccls. and (state adj variable) and (status same (circuits same design)) and (machine same control)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 13:33
L17	556	700/83.ccls. and (machine same control)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 13:55
L18	31	700/83.ccls. and (machine same control) and (status adj data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:06
L19	1	700/83.ccls. and (machine same control) and (status adj data) and (other same elements)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:07
L20	0	700/83.ccls. and (machine same control) and (status adj data) and (other same elements) and (circuits same (design or simulation or simulator or model))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:09
L21	0	700/83.ccls. and (machine same control) and (status adj data) and (other same elements) and circuits	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:09

L22	9	700/83.ccls. and (machine adj control) and (status adj data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:10
L23	8	700/83.ccls. and (machine adj control) and (status adj data) and display	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:10
L24	5	700/83.ccls. and (machine adj control) and (status adj data) and display and circuits	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:10
L25	5	700/83.ccls. and (machine adj control) and (status adj data) and display and circuits and error	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:18
L26	2	700/83.ccls. and (machine adj control) and (status adj data) and display and circuits and error and element	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/17 14:22

4/ 17/ 08 3:07:30 PM

C:\ Documents and Settings\ tstevens\ My Documents\ EAST\ Workspaces\ default.wsp